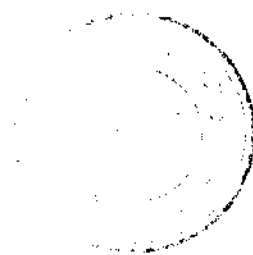


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Foreword

This volume of the Springer Lecture Notes in Computer Science contains most of the papers submitted, accepted for, and presented at the fourth Logic Programming Conference which took place on July 1 to 3, 1985 in Tokyo.

The first conference was held in March, 1982 in Tsukuba Science City, one month prior to the foundation of the Institute for New Generation Computer Technology (ICOT) which sponsored the later conferences. It was felt at that time that since the interests in Prolog were apparently so prevalent and research work had already been widely conducted in various institutions throughout Japan, time had come to organize the Prolog Conference.

For the subsequent years, the name of the conference was changed to "The Logic Programming Conference '8x", its site was moved to Tokyo, and the annual conferences themselves attracted many high quality papers and active audiences.

The conferences were announced and papers were called for only in Japan. The original proceedings contained both papers in Japanese and those in English.

Now, for the fourth conference, the papers written in Japanese were translated into English and all papers published in the present volume were refined.

The last word is my gratitude to authors who contributed their papers for this English version of the proceedings with additional endeavour and to each member of the program committee who spent much of his precious time to organize the conference.

Eiiti Wada
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Table of Contents

Rikio ONAI, Hajime SHIMIZU, Kanae MASUDA, Akira MATSUMOTO and Moritoshi ASO Architecture and Evaluation of a Reduction-based Parallel Inference Machine: <i>PIM-R</i>	1
M. SUGIE, M. YONEYAMA, T. SAKABE, M. IWASAKI, S. YOSHIKUMI, M. ASO, H. SHIMIZU and R. ONAI Hardware Simulator of Reduction-based Parallel Inference Machine: <i>PIM-R</i>	13
T. MARUYAMA, K. HIRATA, H. TANAKA and T. MOTO-OKA A Note on the Elementary Execution Unit in a Parallel Inference Machine	25
H. MATSUDA, M. KOHATA, T. MASUO, Y. KANEDA and S. MAEKAWA Parallel Prolog Machine <i>PARK</i> : Its Hardware Structure and Prolog System	35
Etsuo ITOH and Hiroshi NAKAGAWA Heuristics Applied in Tree Manipulation Algorithm Synthesis	44
Makoto HARAGUCHI Analogical Reasoning using Transformations of Rules	56
T. HISANO and M. SUWA Synchronization and Communication in The ' <i>SUBJECT</i> '	66
H. ITO and H. UENO <i>ZERO</i> : Frame + Prolog	78
Hidehisa TAKAHASHI and Etsuya SHIBAYAMA <i>PRESET</i> - A Debugging Environment for Prolog	90
M. NUMAO and H. MARUYAMA <i>PROEDIT</i> - A Screen Oriented Prolog Programming Environment	100
Shinichi HONIDEN, Naoshi UCHIHARA and Toshiaki KASUYA Software Prototyping with <i>MENDEL</i>	108
H. YOSHIDA, H. KATO and M. SUGIMOTO Retrieval of Software Module Functions Using First-order Predicate Logical Formulae	117

T. AOYAGI, M. FUJITA and T. MOTO OKA Temporal Logic Programming Language <i>Tokio</i> - Programming in <i>Tokio</i>	128
S. KONO, T. AOYAGI, M. FUJITA and H. TANAKA Implementation of Temporal Logic Programming Language <i>Tokio</i>	138
K. NAKAMURA Heuristic Prolog: Logic Program Execution by Heuristic Search	148
Jiro TANAKA, Takashi YOKOMORI and Makoto KISHISHITA AND-OR Queuing in Extended Concurrent Prolog	156
Kazunori UEDA Guarded Horn Clauses	168
Satoru TOMURA TDProlog : An Extended Prolog with Term Description	180
M. KISHIMOTO, T. SHINOGLI, Y. KIMURA and A. HATTORI Design and Evaluation of a Prolog Compiler	192
Akira YAMAMOTO, Masaki MITSUI, Hiroyuki YOSHIDA, Minoru YOKOTA and Katsuto NAKAJIMA The Program Characteristics in Logic Programming Language <i>ESP</i>	204
Kuniaki UEHARA, Takashi KAKIUCHI, Osamu MIKAMI and Jun'ichi TOYODA Extended Prolog and its Application to an Integrated Parser for Text Understanding	214
H. SUZUKI, M. KIYONO, S. KOUGO, M. TAKAHASHI, S. MOTOIKE and T. NIKI A Travel Consultation System : Towards a Smooth Conversation in Japanese	226
J.M. CHOI, M.S. SONG, K.J. JEONG, H.C. KWON, S.Y. HAN and Y.T. KIM A Prolog-based Korean-English Machine Translation System and its Efficient Method of Dictionary Management	236

Masahiro FUJITA, Makoto ISHISONE, Hiroshi NAKAMURA, Hidehiko TANAKA and Tohru MOTO-OKA Using the Temporal Logic Programming Language <i>Tokio</i> for Algorithm Description and Automatic CMOS Gate Array Synthesis	246
Yasunori NODA, Tetsuo KINOSHITA, Akira OKUMURA, Tatsuro HIRANO and Tadashi HIRUTA A Parallel Logic Simulator based on Concurrent Prolog	256
I. NAGASAWA A Method of Representing Processes in a Constraint Solver	266
Katsumi NITTA and Juntaro NAGAO <i>KRIP</i> : A Knowledge Representation System for Laws relating to Industrial Property	276
Yasushi MATSUMURA, Takashi MATSUNAGA, Yusuke MAEDA, Shusaku TSUMOTO, Hiroshi MATSUMURA and Michio KIMURA Consultation System for Diagnosis of Headache and Facial Pain: ' <i>RHINOS</i> '	287
Isao SUGIYAI and Keiko ISHIKAWA Knowledge Realization and Transformation in <i>KRISP</i>	299

Architecture and Evaluation of a Reduction-Based Parallel Inference Machine : PIM-R

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ABSTRACT

This paper proposes a Reduction-based Parallel Inference Machine : PIM-R and describes the architecture and its evaluation using two kinds of software simulators. Target languages of PIM-R are Prolog and Concurrent Prolog. PIM-R executes Prolog programs in OR parallel and Concurrent Prolog programs in AND parallel. The simulation results show that PIM-R is able to exploit the parallelism in Prolog and Concurrent Prolog programs.

1 INTRODUCTION

Currently there are several proposals for parallel inference machine architecture (Moto-oka 84 ; Ito 84) and predicate logic languages (Shapiro 83 ; Clark 84 ; Pereira 84). We have chosen Prolog and Concurrent Prolog (Shapiro 83) as the target languages of PIM-R. These have been selected by ICOT as the base languages for its Kernel Language version 1 (KL1(84)). The basic operation of PIM-R consists of parallel generation of new resolvents. PIM-R executes Prolog programs in OR parallel and Concurrent Prolog programs in AND parallel. In PIM-R, if a process has multiple goals (the multiple goals, as a whole, are called the parent process), only the reducible goals, specified by various operators, are copied and reduced. Each resolvent generated contains a pointer to its parent process; the solution obtained is returned to the parent process using the pointer. That is, PIM-R executes Prolog and Concurrent Prolog programs by expanding and reducing a process tree. When the processing ends, the tree is logically deleted.

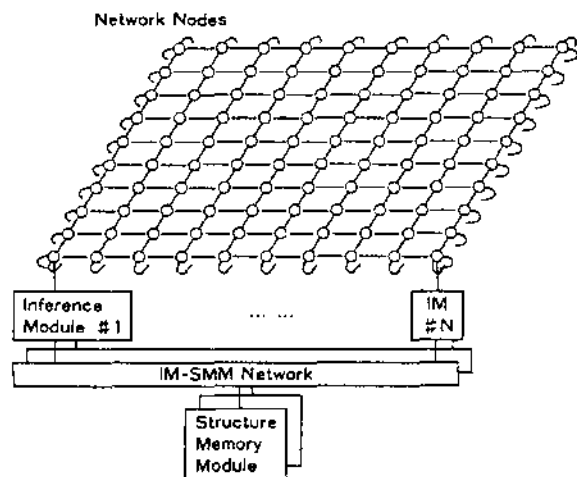


Fig. 1 Conceptual configuration of PIM-R

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