

# **ADVANCES IN CIRCUITS AND SYSTEMS**

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OF CHINA 1985  
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CONFERENCE ON  
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# **ADVANCES IN CIRCUITS AND SYSTEMS**

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# **ADVANCES IN CIRCUITS AND SYSTEMS**

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**1985 ICCAS  
PROGRAM SCHEDULE**

|                               |           | Inaugural Session, Room: "GREAT WALL"                       |                                            |                                                          |                                                          |
|-------------------------------|-----------|-------------------------------------------------------------|--------------------------------------------|----------------------------------------------------------|----------------------------------------------------------|
| Monday,<br>June 10            | Morning   |                                                             |                                            |                                                          |                                                          |
|                               | Afternoon | S-1<br>VLSI Design-Testing<br>and Applications              | S-5<br>Graph Theory and<br>Applications I  | S-9<br>Digital Filters and<br>Processing I               | S-13<br>Active Networks                                  |
| Tuesday,<br>June 11           | Morning   | S-2<br>VLSI Layout                                          | S-6<br>Graph Theory and<br>Applications II | S-10<br>Digital Filters and<br>Processing II             | S-14<br>Network, Termina-<br>tions and Power<br>Transfer |
|                               | Afternoon | S-3<br>VLSI Layout and<br>Design                            | S-7<br>Fault Analysis I                    | S-11<br>Signal Processing<br>and Distributed<br>Networks | S-15<br>Circuit Simulation<br>and Analysis I             |
| Wednesday, Morning<br>June 12 |           | S-4<br>Noise Reduction and<br>Switched Capacitor<br>Filters | S-8<br>Fault Analysis II                   | S-12<br>Communication Cir-<br>cuits and Systems          | S-16<br>Circuit Simulation<br>and Analysis II            |
|                               | Room      | "BEIJING"                                                   | "SHANGHAI"                                 | "GUANGZHOU"                                              | "HANGZHOU"                                               |
|                               |           |                                                             |                                            |                                                          | "XIAN"                                                   |

## TECHNICAL SESSIONS

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#### Keynote Address

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Speaker: M. E. Van Valkenburg, University of Illinois,  
U. S. A

#### Panel Discussion

- IS. 2 "Trends in Circuits and Systems" 3  
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Berkeley, U. S. A.  
Panelists: S. W. Director, Carnegie Mellon  
University, U. S. A;  
S. L. Hakimi, Northwestern University,  
U. S. A;  
B. Liu, Princeton University, U. S. A;  
E. Lueder, University of Stuttgart,  
Germany;  
T. Ohtsuki, Waseda University, Japan;  
R. A. Rohrer, Scientific Calculations,  
U. S. A;  
M. E. Van Valkenburg, University of  
Illinois, U. S. A.

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Chairman: S. W. Director, Carnegie Mellon Univer-  
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Tsinghua University, Beijing, China.  
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1.4\* "An Environment for VLSI Logic Synthesis" Invited 4  
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Chairman: T. Ohtsuki, Waseda University, Japan  
S. J. Wang, Institute of Semiconductors  
Academia Sinica, China

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\* Manuscript not available for publication.

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*Chairman: J. K. Wu, Institute of Computing Technology, Academia Sinica, China;  
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- |     |                                                                                                                                                                                                                                                                         |    |
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- |     |                                                                                                                                                                                               |     |
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