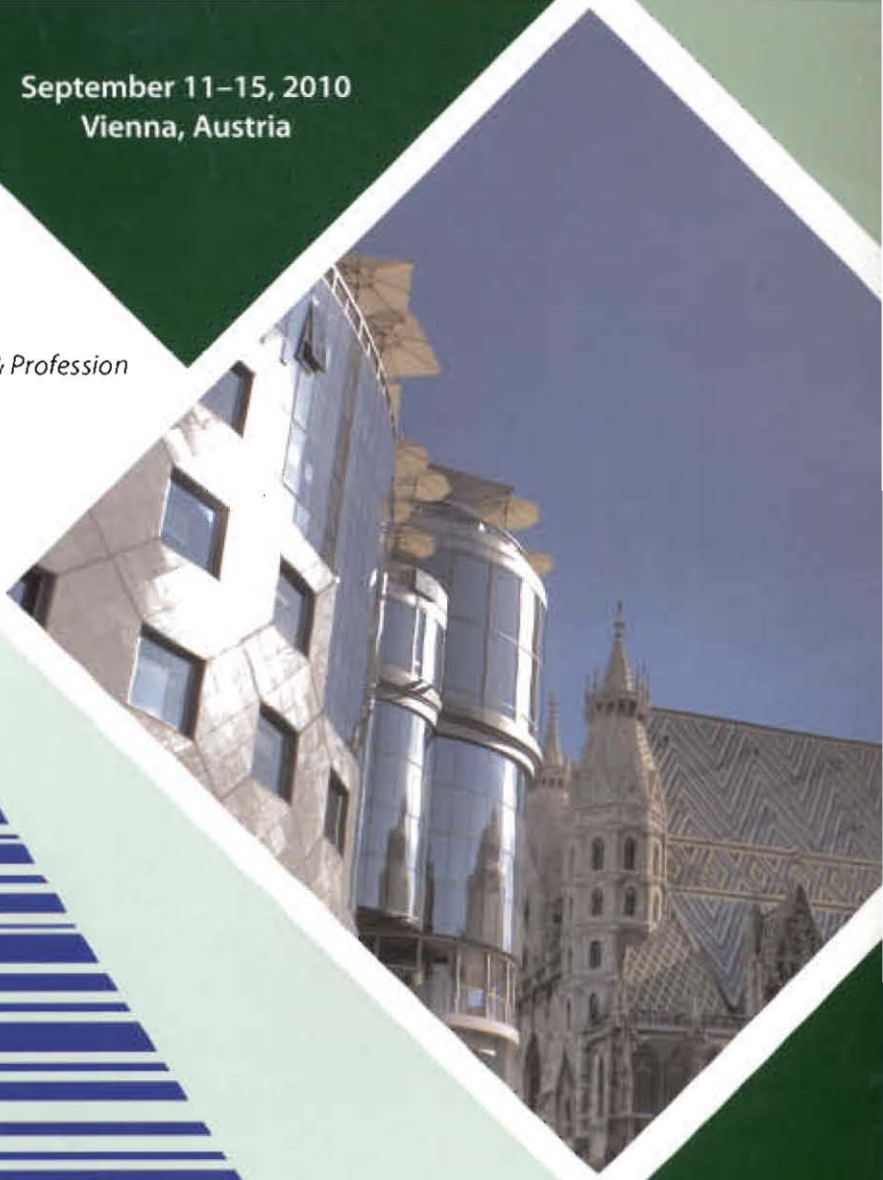
September 11–15, 2010
Vienna, Austria

Association for
Computing Machinery

Advancing Computing as a Science & Profession



PACT'10

Proceedings of the Nineteenth International Conference on
Parallel Architectures and Compilation Techniques

Sponsored by:

ACM SIGARCH, IEEE TCPP and TCCA, and IFIP WG10.3

Supported by:

AMD, Hewlett-Packard, IBM, IBM Research, Intel, NSF, Power.org, Qualcomm, Reservoir Labs, and City of Vienna

Table of Contents

Session 2A: Caches & Coherence I

Session Chair: Pedro Trancoso *(University of Cyprus)*

Session 2B: Parallelization and Parallel Programming I

Session Chair: Rudolf Eigenmann *(Purdue University)*

Session 3A: Heterogeneous Platforms and Frameworks

Session Chair: Calin Cascaval *(Qualcomm Research)*

Session 3B: Scheduling and Design Optimization

Session Chair: Antonia Zhai *(University of Minnesota)*

Session 4: Best Papers

Session Chairs: Michael Gschwind *(IBM Systems and Technology Group)*,
Jens Knoop *(Technische Universität Wien)*

Session 5A: Languages and Compilers

Session Chair: Vivek Sarkar *(Rice University)*

Session 5B: ACM Student Research Competition

Session Chairs: M. Anton Ertl & Franz Puntigam *(Techische Universität Wien)*

Session 6A: Parallelization and Parallel Programming II

Session Chair: David Gregg *(Trinity College Dublin)*

Session 6B: Speculation

Session Chair: Jim Dehnert *(Google)*

Session 7A: Caches and Coherence II

Session Chair: Aamer Jaleel *(Intel)*

Session 7B: Data Distribution and Tiling

Session Chair: Lawrence Rauchwerger *(Texas A&M University)*

Poster Session

Session Chair: Michael Franz *(University of California at Irvine)*