CAPACITY DESIGN

Royal Scientific Society

Computer Technology, Training &
Industrial Studies Centre

BIBLIOTHEQUE DU CERIST

Capacity Design

CONTENTS

	Page
1. NECESSITY OF MEMORY ESTIMATION	2
2. OUTLINE OF MEMORY ESTIMATION	4
2.1 Memory Estimation Difficulty	4
2.2 Memory Estimation Procedure	6
3. ESTIMATING THE SYSTEM AREA SIZE	10
4. ESTIMATING THE DISCRETE AREA SIZE	12
4.1 Procedures for Estimating the Discrete Area Size	12
4.2 Estimating the Module Sizes	14
4.2.1 Estimating the Number of Program Steps	16
4.2.2 Estimating the Module Sizes	18
4.2.3 Determining the Load Segment (segmentation)	20
4.3 Estimating the Resident Area Size	30
4.4 Estimating the Transient Area Size	36
4.4.1 Average Number of Segments Being Used and Segment Using Time	40
4.4.2 Estimating the Fluctuation Range	54
4.4.3 Comparison with the Largest Segment	68
4.5 Estimating the Work Area Size	70
4.6 Estimating the Allowance	76
4.7 A Simple Way to Estimate Memory	82
5. MEMORY ESTIMATION AND APPROXIMATION	92

BIBLIOTHEQUE DU CERIST