

# OPCIO: Optimizing Power Consumption for Embedded Devices via GPIO Configuration

XIAOYU JI, College of Electrical and Engineering, Zhejiang University, China

XINYAN ZHOU, Faculty of Electrical Engineering and Computer Science, Ningbo University, China

MIAO XU, University of South Carolina, USA

WENYUAN XU, College of Electrical and Engineering, Zhejiang University, China

YABO DONG, College of Computer Science and Technology, Zhejiang University, China

Battery lifetime is one of the main challenges that impedes the deployment of energy-constrained wireless networks, such as unattended Internet-of-Things (IoT) systems. To prolong battery lifetime, the duty-cycle mode is utilized in many IoT systems, especially in environment monitoring Wireless Sensor Networks (WSN) and Low-Power Wide-Area Networks (LPWAN). In duty-cycle mode, devices transmit packets during the active phase, which lasts for a short time, and sleeps the rest of the time. Prior research mainly focuses on energy efficiency in the active phase; energy consumption during the sleep phase, however, is always ignored, as it is assumed to have little margin to be optimized. In this work, we reveal that sleep phase can become a significant battery consumer due to the misconfiguration of General-Purpose Input/Output (GPIO) pins of micro-controllers. We propose OPCIO, which incorporates a genetic algorithm to obtain energy-efficient GPIO configurations automatically to squeeze the energy waste during the sleep phase. We prototype OPCIO on off-the-shelf devices and evaluate it on two ARM devices. Experiment results show that OPCIO can effectively find multiple low-power configurations that prolong the lifespans up to 10 $\times$ .

CCS Concepts: • **Hardware** → **Wireless integrated network sensors; Power estimation and optimization;**

Additional Key Words and Phrases: IoT systems, automated GPIO configuration mechanism, low-power configuration

## ACM Reference format:

Xiaoyu Ji, Xinyan Zhou, Miao Xu, Wenyuan Xu, and Yabo Dong. 2020. OPCIO: Optimizing Power Consumption for Embedded Devices via GPIO Configuration. *ACM Trans. Sen. Netw.* 16, 2, Article 16 (January 2020), 28 pages.

<https://doi.org/10.1145/3373417>

This work is supported by China NSFC Grant 61702451, ZJNSF Grant LGG19F020020, Zhejiang Provincial Natural Science Foundation of China under Grant No. LQ20F020012, and the Fundamental Research Funds for the Central Universities 2019QNA4027.

Authors' addresses: X. Ji and W. Xu (corresponding author), College of Electrical and Engineering, Zhejiang University, 38 Zheda Rd, Hangzhou, Zhejiang, 310027, China; emails: {xji, wyxu}@zju.edu.cn; X. Zhou (corresponding author), Faculty of Electrical Engineering and Computer Science, Ningbo University, 818 Fenghua Rd, Ningbo, Zhejiang, 315211, China; email: zhouxinyan@nbu.edu.cn; M. Xu, University of South Carolina, Columbia, SC; email: xum@email.sc.edu; Y. Dong, College of Computer Science and Technology, Zhejiang University, 38 Zheda Rd, Hangzhou, Zhejiang, 310027, China; email: ybdong@zju.edu.cn.

Permission to make digital or hard copies of all or part of this work for personal or classroom use is granted without fee provided that copies are not made or distributed for profit or commercial advantage and that copies bear this notice and the full citation on the first page. Copyrights for components of this work owned by others than ACM must be honored. Abstracting with credit is permitted. To copy otherwise, or republish, to post on servers or to redistribute to lists, requires prior specific permission and/or a fee. Request permissions from [permissions@acm.org](mailto:permissions@acm.org).

© 2020 Association for Computing Machinery.

1550-4859/2020/01-ART16 \$15.00

<https://doi.org/10.1145/3373417>